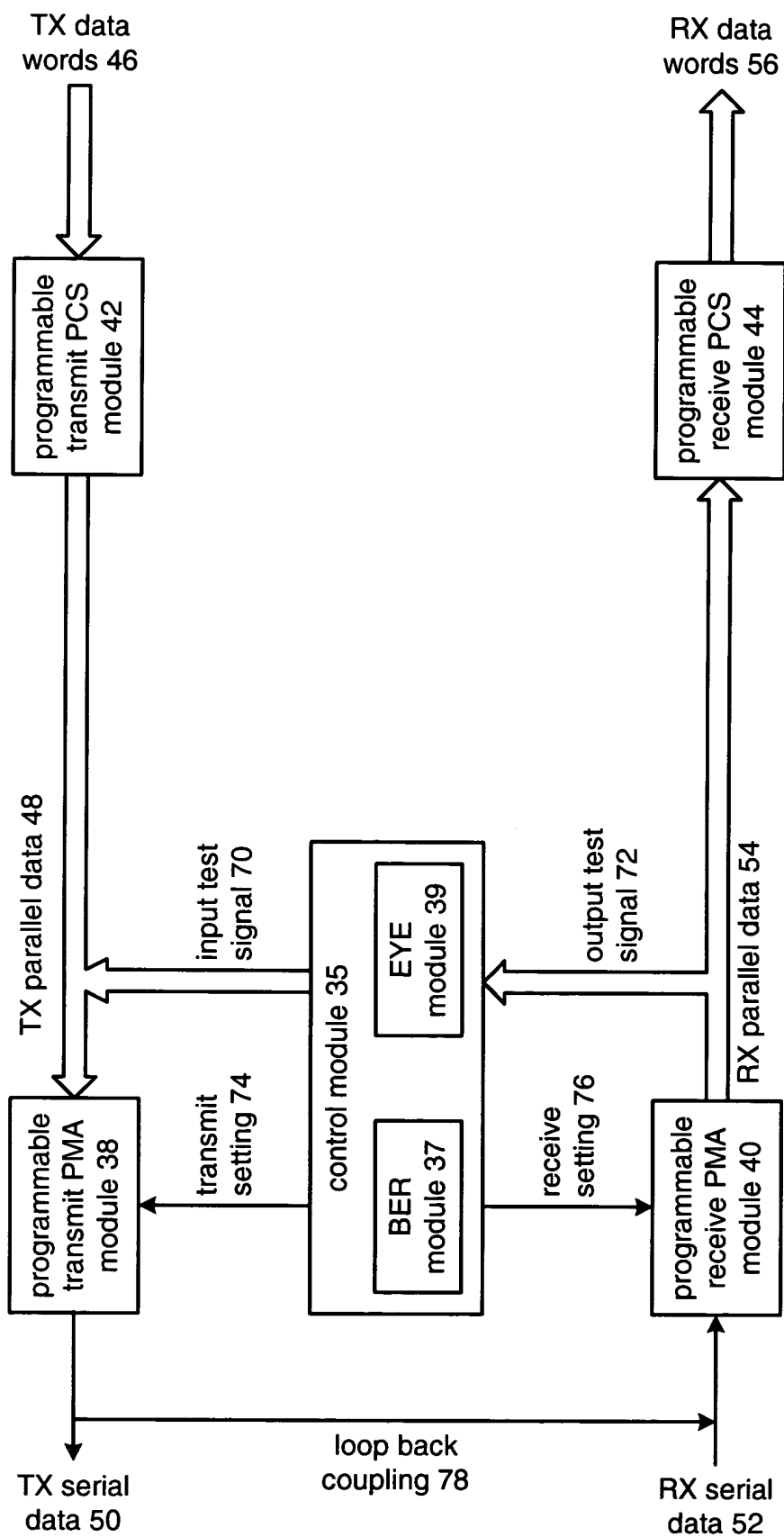
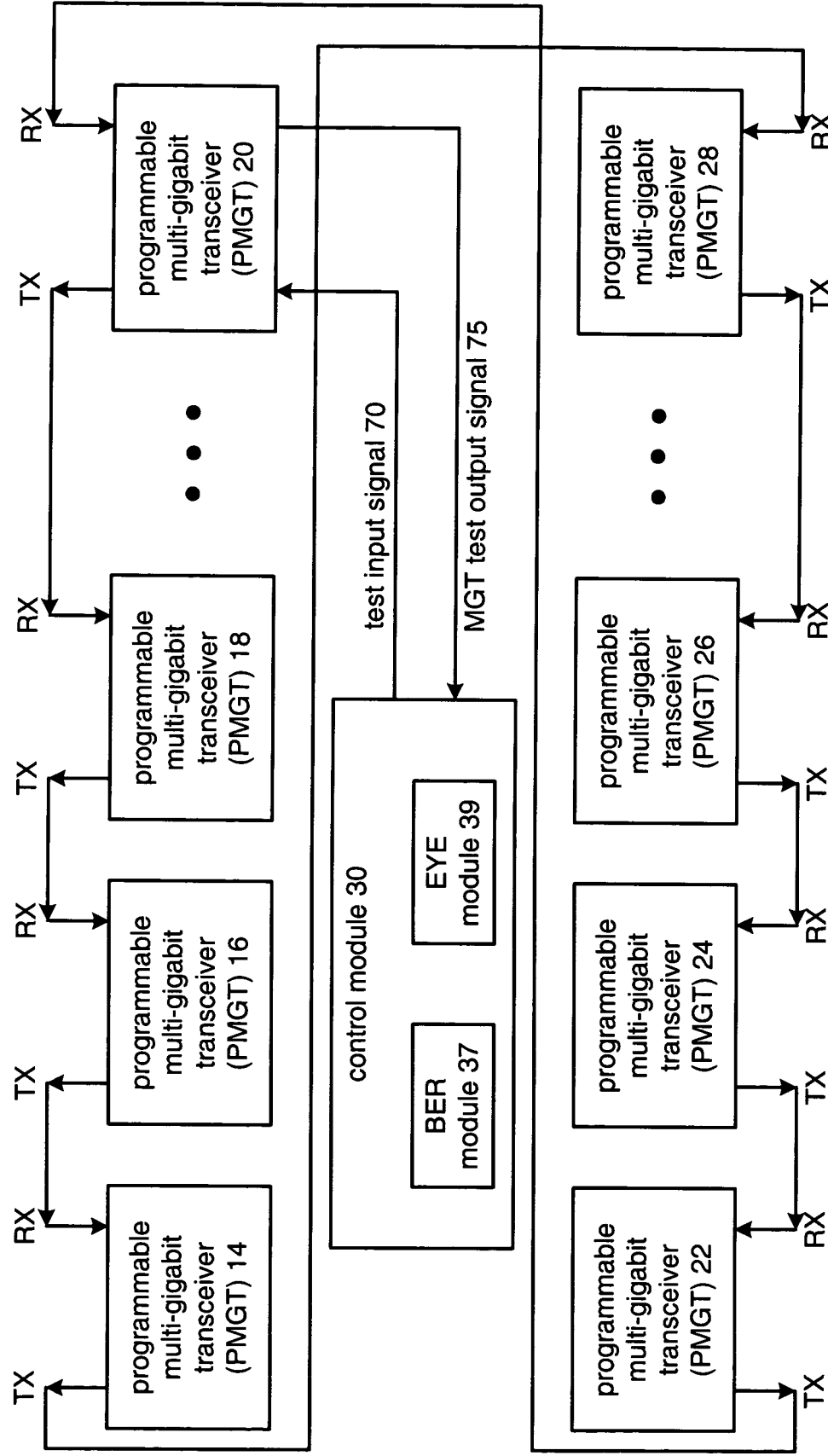


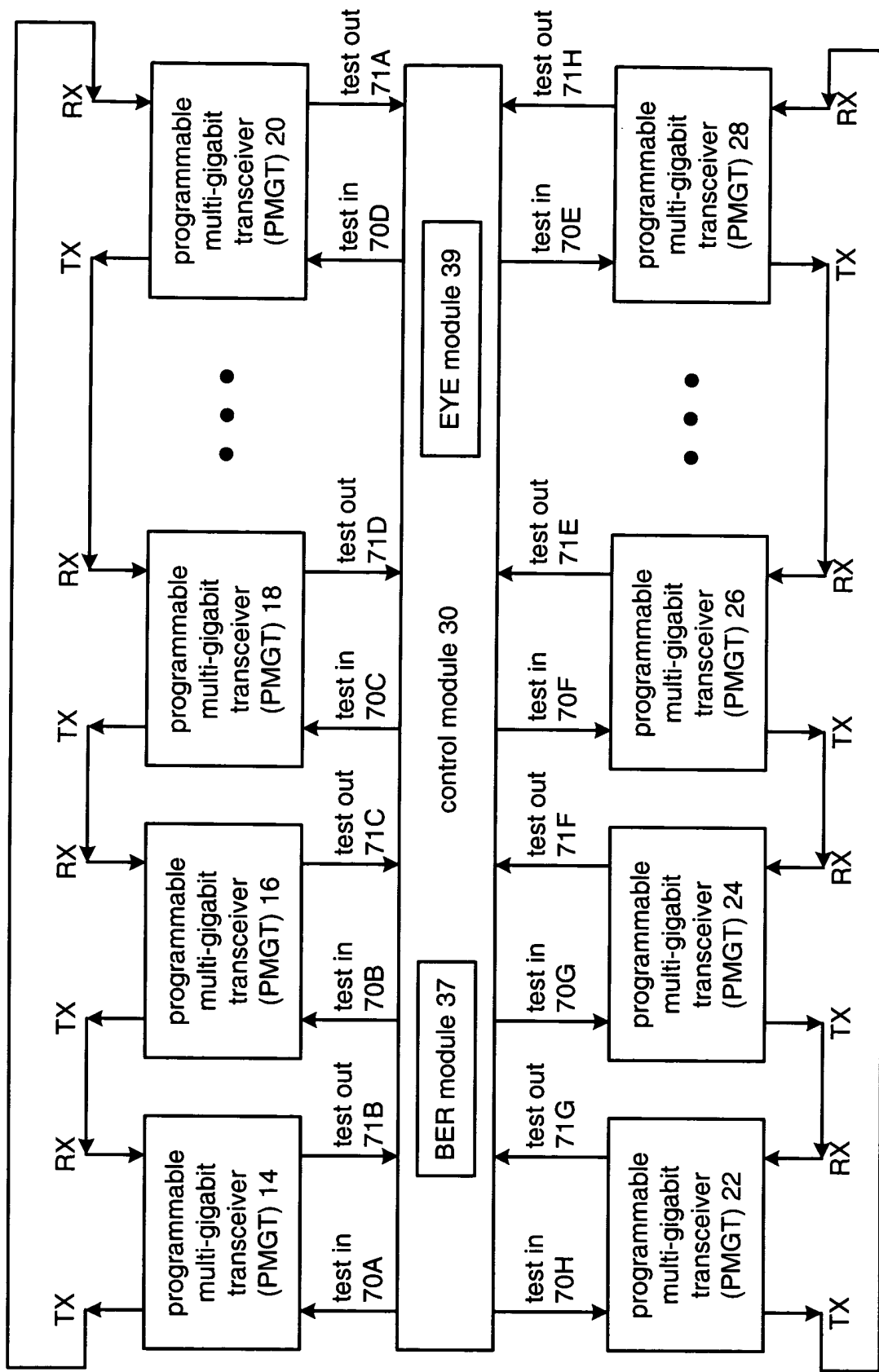
**Figure 1**  
programmable  
logic device 10



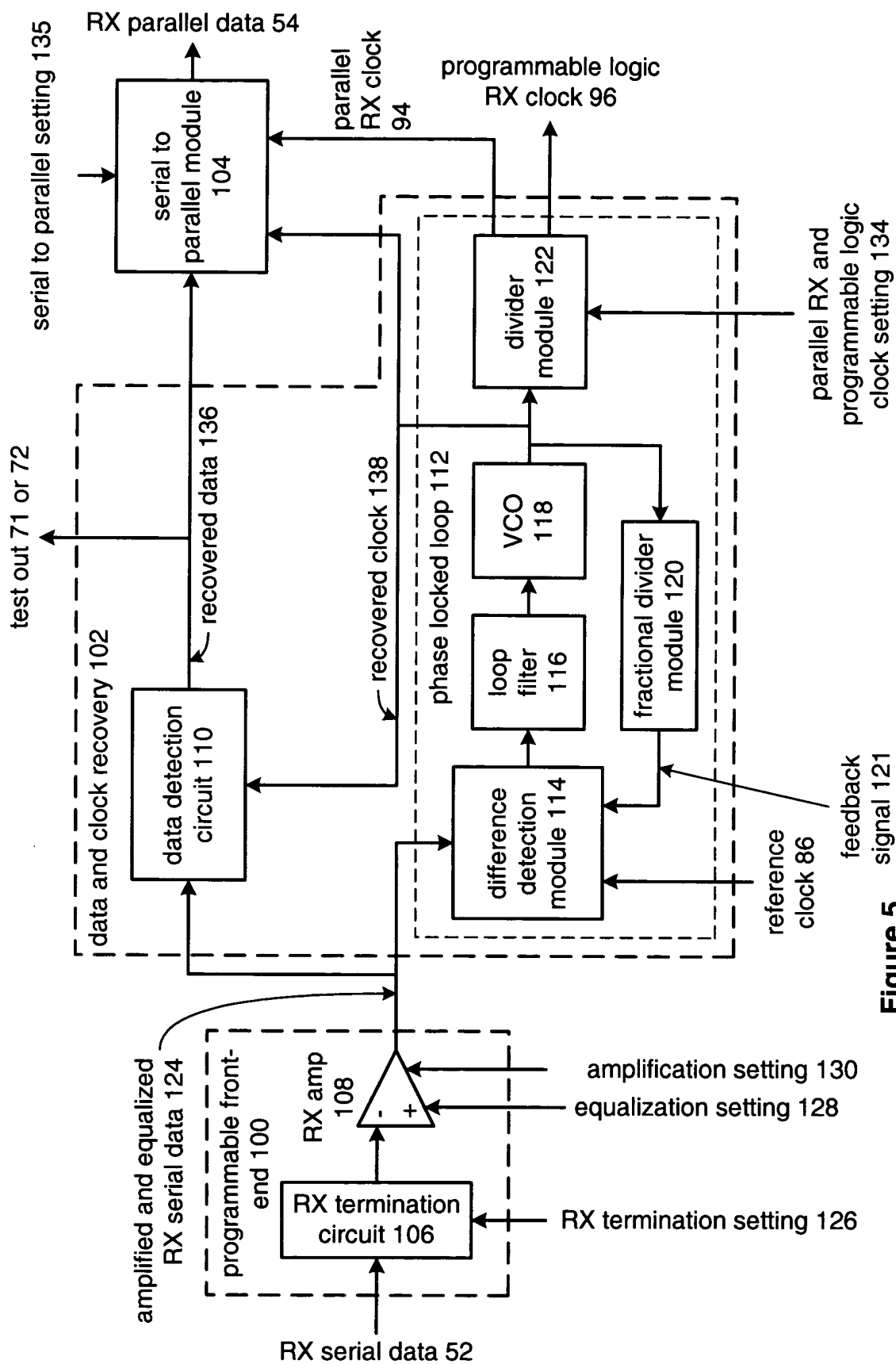
**Figure 2**  
**programmable MGT 14 - 28**



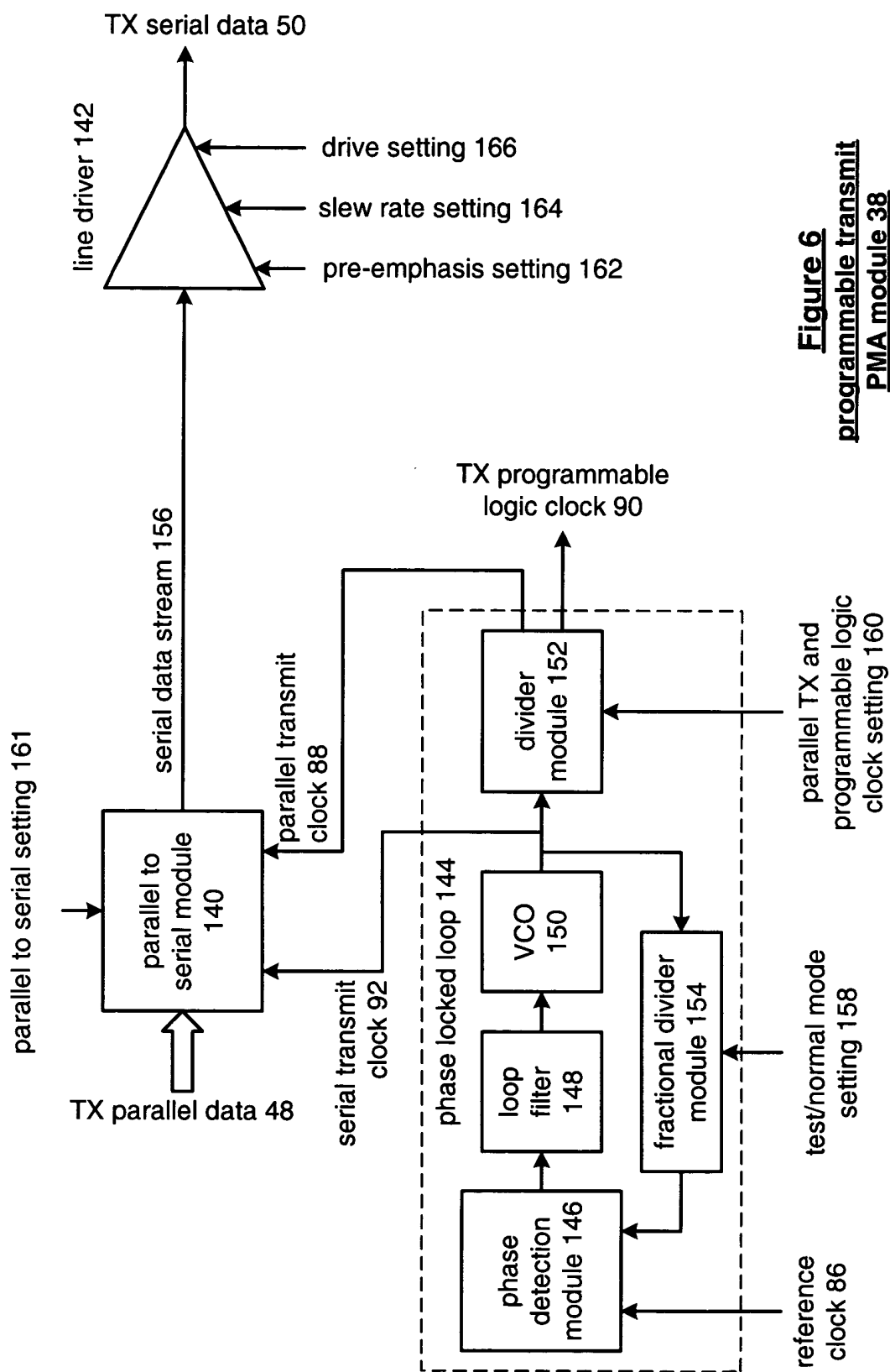
**Figure 3**  
overall loop back daisy  
chain coupling 80



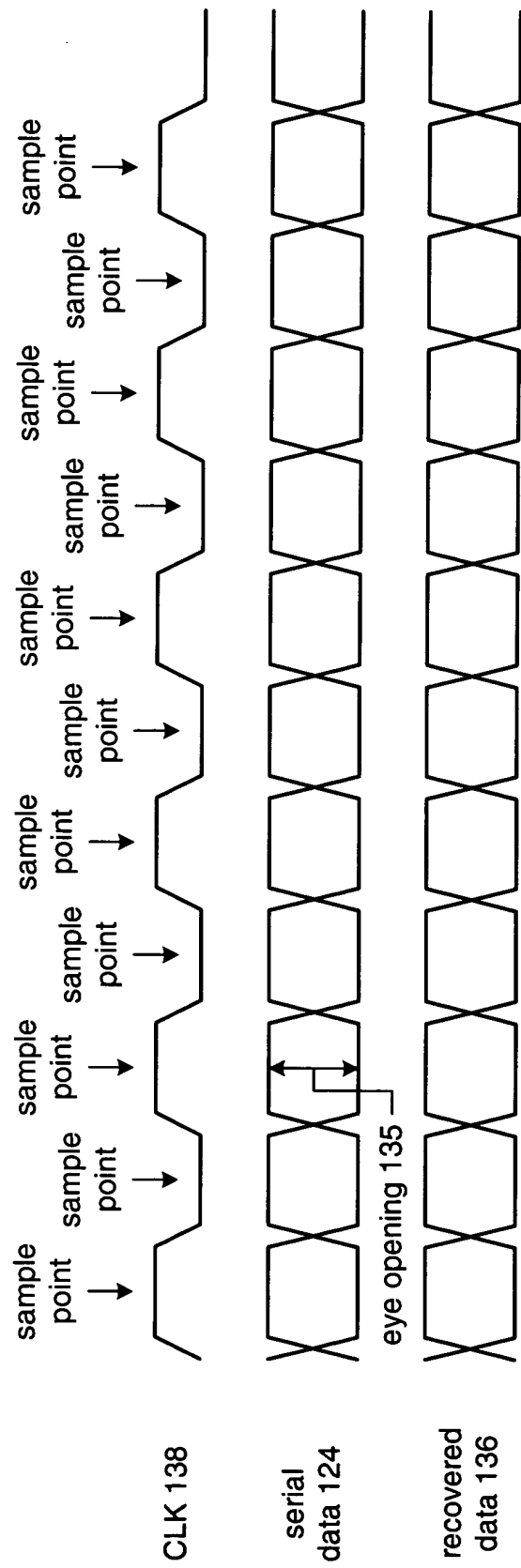
**Figure 4**  
MGT to MGT daisy  
chain coupling 82



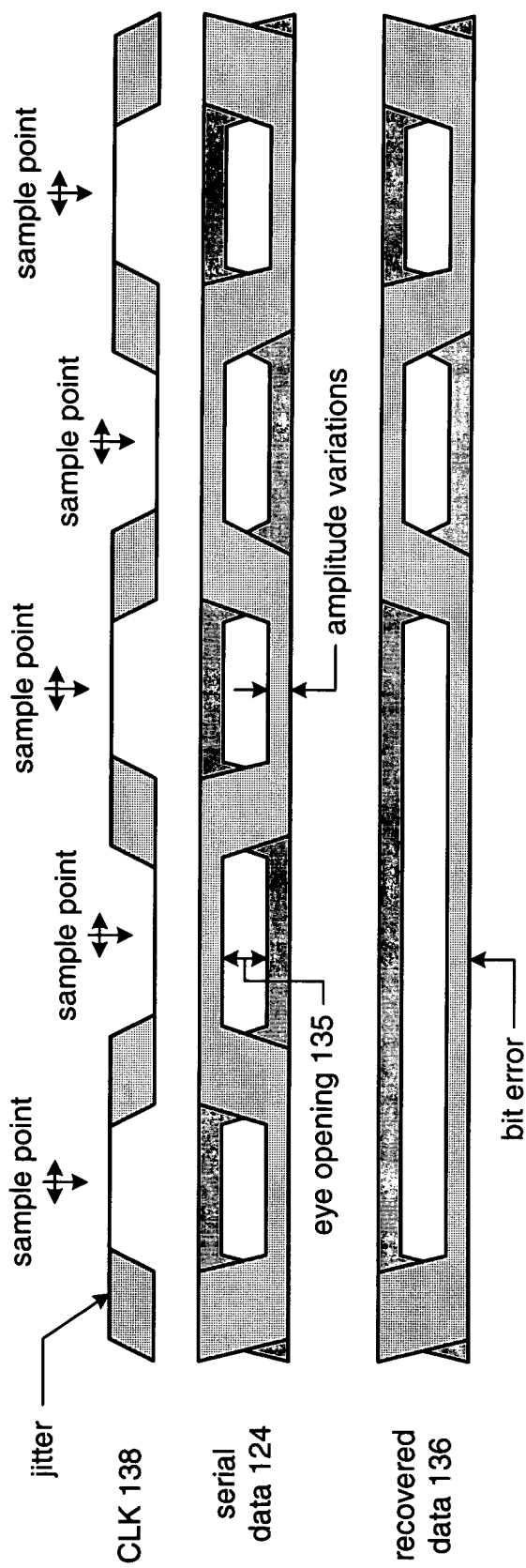
**Figure 5**  
programmable receive  
PMA module 40



**Figure 6**  
programmable transmit  
PMA module 38



**Figure 7: ideal timing diagram of data recovery**



**Figure 8: actual and test timing diagram of data recovery**